

Novel True-Kelvin MEMS Analytical DC Probes to enable Accurate and Repeatable Characterization of Advanced-Node devices for AI applications



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Outline

- Generative AI Applications
- Foundation Pillar in the Rise of AI Semiconductor Technologies
- Accurate Wafer Tests with True-Kelvin MEMS Probes
- Conclusion
- Acknowledgement

Generative AI Applications



ChatGPT (LLM for Human-Like Interaction)



Gen AI in Finance (Fraud Prevention & Risk Management)



GAN (Gen Data for Model Training)



Anomaly Detection Medical Imaging



Precision Agriculture (Yield, Pest & Disease Management)



Image & Video Generation (Efficient Gen High Quality Content)

https://hospitalityinsights.ehl.edu/chatgpt-overview ; https://www.linkedin.com/pulse/generative-adversarial-networks-gans-revolutionizing-artificial-s/ ; https://compai-lab.github.io/teaching/anomaly_seminar/ https://www.bitsathy.ac.in/blog/generative-ai-in-agriculture-revolutionizing-crop-management/ ; https://duolookmedia.com/text-to-image-artificial-intelligence-reshapes-marketing/

Al Image Generation



AI Applications are made possible by...

- **1. Innovations in Efficient Machine-Learning Algorithms**
- 2. Availability of Massive Data to train Neural Networks
- 3. Energy-Efficient Computing through Advanced Semiconductor Technologies

AI systems vs Semiconductor Technologies



1998 2000 2002 2004 2006 2008 2010 2012 2014 2016 2018 2020 2022 2024

Advances in semiconductor technology [top lines]—including new materials, advances in lithography, new types of transistors, and advanced packaging—have driven the development of more capable AI systems [bottom line]

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From Integrated Devices to Integrated Chiplets

- ↓ Feature Size, ↑ Device Density.
- Reticle Limit Unable to make ICs > 800 mm², 28.28×28.28mm.
- Attaching several chips onto a larger interposer.
- TSMC's chip-on-wafer-on-substrate (CoWoS) technology.
 - 6 reticle fields' worth of compute chips, along with a dozen high-bandwidth-memory (HBM) chips
- Nvidia's Ampere (54B Transistors), Hopper (80B T), Blackwell (208B T).
- Tens of thousands of these processors needed to train ChatGPT.





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Source: Nvidia.

Towards GPUs with 1 Trillion Transistors

- Transistors per processor.
 - 1.73x every 2 years
- Towards 1 Trillion Transistors by 2030!



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New Transistor Architecture – ↑ Energy Efficiency



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The Silicon Age: Trends in Semiconductor Devices Industry, Journal of Engineering

Accurate Wafer IV Tests are Crucial

- Transistor technology is the Foundational Pillar driving AI applications
- Development of GPUs
 - Accurate SPICE, Device Reliability tests are needed.
 - 1st Pass Product Design Success and Reliable Performance.
- Urgent need to overcome Test Challenges involving Advanced Node devices.
- Trends and Challenges
 - $V_D \downarrow$
 - $V_{TH} \downarrow$
 - $R_{DS} \downarrow$

 - Slotted Cu / Al capped test pads
 - Test Temperature Range \uparrow
 - Total number of Devices to Test & Model \uparrow
 - Requires Accurate & Repeatable Wafer Measurements

Better engineering probes are needed to address challenges!

Mechanical Design & Performance

Overview of Novel True Kelvin MEMS Probe

Engineering Probes:

- 1. Quasi-Kelvin: Single cantilever probe
- 2. True-Kelvin: Dual MEMS probe

These probes deliver fA-level measurements capability over a wide range of temperature for advanced characterization and reliability testing.



Quasi-Kelvin Cantilever vs True-Kelvin MEMS Probes

Quasi-Kelvin Cantilever



True-Kelvin MEMS Probe



Parasitic Resistance Fully Corrected

Probe Scrub versus Z Over Travel



Probe Scrub (µm)

ltem	Cantilever	MEMS
Recommended Z Over Travel	75µm	20µm
Probe Scrub Ratio	40%	8%
Probe Scrub	30µm	6-7µm
Slotted Cu Pads Handling	Tip Bending Observed	No Tip Bending

Benefits of using MEMS Probe:

- Small scrub mark → Smaller pad size
- Lighter probe force → Less pad damage
- Repeatable measurement is possible



Setup of 4 True Kelvin MEMS Probes



Test Setup:

- 1. Four positioners/probes are set in NW, NE, SW and SE positions.
- 2. True kelvin probes are used and probed on 30x30um pad size.
- 3. New tool is used for easy probe tip setup and replacement without removing positioners.

Over-Temperature PTPA on 30um Test Pads

- 1. Autonomous DC Solution: Track all probes which are mounted on the motorized positioner. Thermal expansion can be automatically corrected.
- 2. Performance Analyzer: Software to analyze the minimum pad size capability.
- Test Condition: 20TDs each at 25, 85, 150 and -40C as temperature.
 20TDs each x 4 pads x 4 different temperature = 320 total TDs.
- 4. Test Results: Achieving to contact all probes within 30x30um pad size.

Measurement Principles & Electrical Performance

Principle of Guarding - Triaxial vs Coaxial

Regular Coax Cable I_{Leakage} = 100 nA

Triaxial Cable (Guarded) $I_{Leakage} = 1 \text{ fA}$

True-Kelvin MEMS Probe – Probe Leakage

- Probe Leakage is critical for device offstate current measurements.
- Excellent leakage performance at 25 and 175°C.
- Guard design worked very well.

Principle of Kelvin Measurements

- Kelvin Measurement eliminates parasitic resistance
 - Resistance of Cables, Connectors, Probe Holders & Probes.
 - Ensure Accurate Bias Voltage as close to device as possible!

Probe Parasitic Resistance

Quasi-Kelvin Cantilever Probes

- Parasitic R 1 Ω at 25°C ; 5-10 Ω at 150°C
 - 25°C vs 150°C = Parasitic R gap, Not Acceptable
- 30th Contact Cycle Open Circuit, Cu oxidation.

True-Kelvin MEMS Probes

- Al/Cu pads, 1st 50 contact cycles, < 5 m Ω
 - 25°C vs 175°C = no gap in Parasitic R.
- Gold pads, \approx 1-3 m Ω Rc, No metal oxidation.

2 Quasi-Kelvin Cantilever Probes R on Same Al/Cu DUT Pad

2 True-Kelvin MEMS Probes R on Same Gold Pad 1.E+00 1.E-01 Resist 1.E-02 Seri 1.E-03 1.E-04 10 20 90 100 ----- 25 Deg C Au Pad Number of Contact Cycles 175 Deg C Au Pad 2 True-Kelvin MEMS Probes R on Same Al/Cu DUT Pad 1.E+01 Tip Width=6 μm (ohms) 1Ω 1.E+00 Pitch=20 µm 1.E-01 Resi Series 1.E-02 $1 \text{m}\Omega$ 1.E-03 100 90 Number of Contact Cycles - 25 Deg C DUT Al Pad

Accuracy of True-Kelvin MEMS probes

Drain Source Resistance vs Error	Quasi-Kelvin Cantilever Probe	True-Kelvin MEMS Probe
Worst Case Parasitic R of 2 Probes @ High Temp on Al-Cu Pads	~10 ohms	3 milli-ohms
Error @ Device R _{DS} = 200 Ohm	5 %	0.0015 %
Error @ Device R _{DS} = 100 Ohm	10 %	0.0030 %
Error @ device R _{DS} = 10 Ohm	100 %	0.0300 %
Error @ device R _{DS} = 2 Ohm	500 %	0.1500 %
Error @ device R _{DS} = 1 Ohm	1000 %	0.3000 %

True-Kelvin MEMS Probes can...

- Accurately characterize advanced node transistors with ultra-low R_{DS}.
- Eliminate Errors introduced by Parasitic & Contact resistance of probes.

IV Measurements with TK MEMS Probe @ 25°C over 100 Contacts

Quasi-Kelvin Cantilever Probes @ 25°C

True-Kelvin MEMS Probes* @ 25°C

IV @ 25 & 175°C, 100 Contacts Cycles

• True-Kelvin MEMS Probe

- Eliminate Probe R_{Parasitic}
- Eliminate Probe RC on pads.

• Achieve Accurate and Repeatable measurements.

Raw Test Data, Id & Rds @ 175°C

Drain Current Id over 10 Contact Cycles

Channel Resistance Rds over 10 Contact Cycles

0.00 4.36E-06 4.32E-06 6.92E-06 3.89E-06 4.46E-06 2.98E-06 3.10E-06 3.11E-06 8.49055 8.48911 8.48174 8.49176 8.49020 8.48951 8.48951 8.49555 8.49204 0.00 0.00589 0.00569 0.02569 0.	8.4948 8.75534 9.08898 9.49776 9.9847 10.5471 11.1812 11.8815
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1.20 0.04723 0.04724 0.04724 0.04722 0.	25.4211

Probe Cleaning vs Touchdown

0.9

Quasi-Kelvin **MEMS** Probe

- Abrasive Probe cleaning needed
- Deeper Probe Z overtravel
- True-Kelvin **MEMS** Probe
 - Minimal Probe Cleaning
 - Low and stable Rc (< $3m\Omega$)

CV & 1/f Noise Tests

Conclusion

- Emerging GenAl Applications.
- Transistor Device is Foundational Pillar driving Al applications.
- True Kelvin MEMS Probes for Advanced-Node Tests
 - Small Probe Scrub
 - Little Pad Damage
 - Minimal Probe Cleaning
 - Accurate and Repeatable IV measurements
 - Good correlations for CV and 1/f Noise measurements
 - Support Micro-Bump tests
 - Low Cost of Test with Long Lifetime

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Thank You! Questions?

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